



FIG. 1A

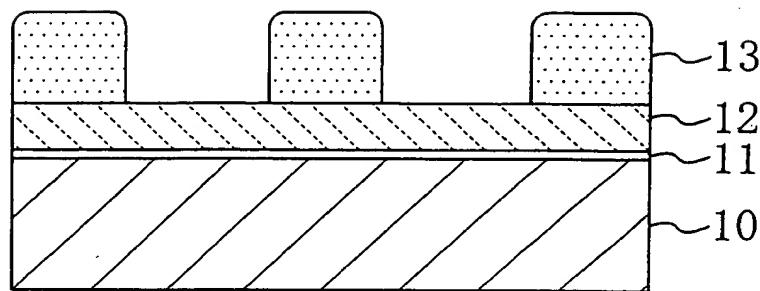


FIG. 1B

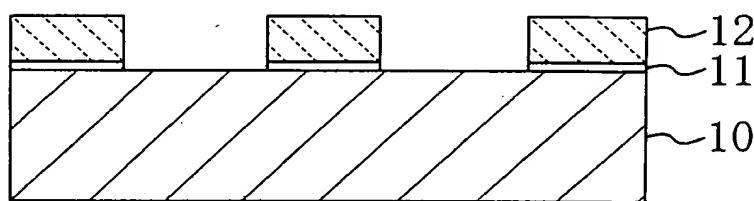


FIG. 1C

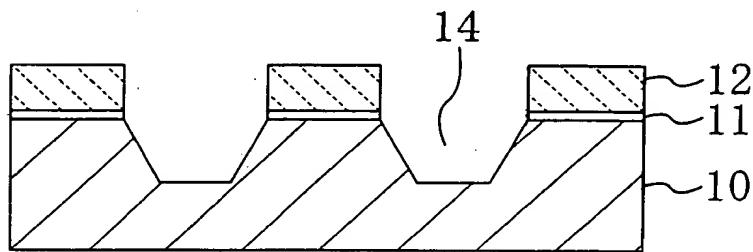


FIG. 1D

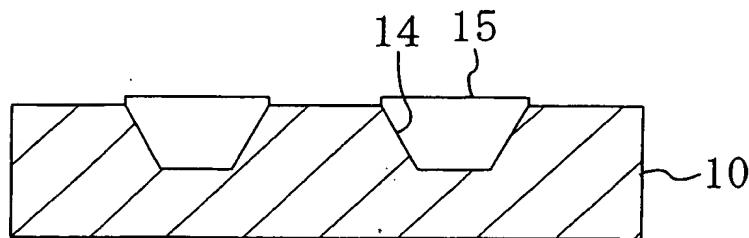




FIG. 2

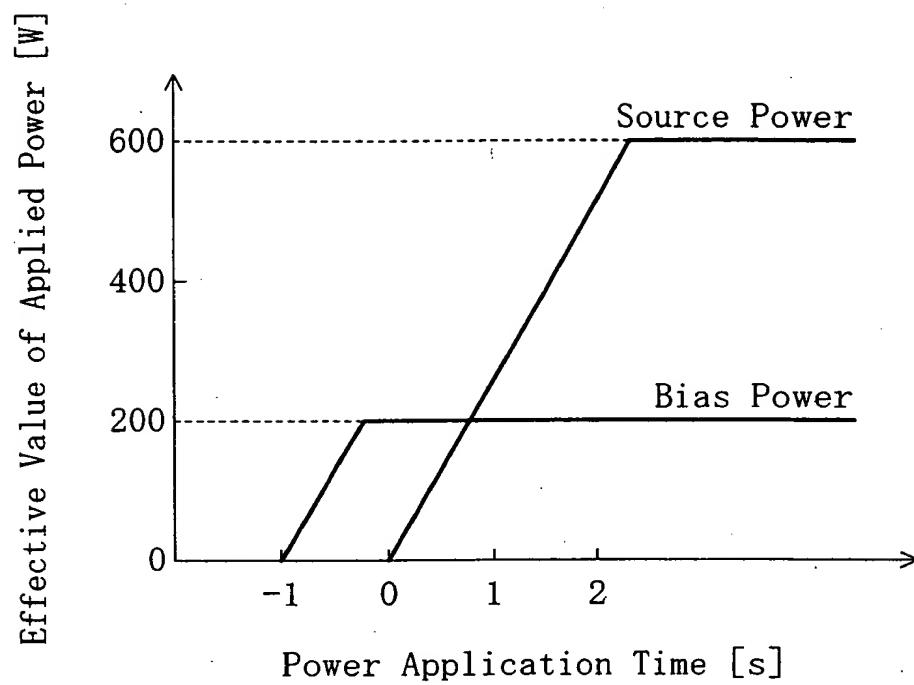




FIG. 3A

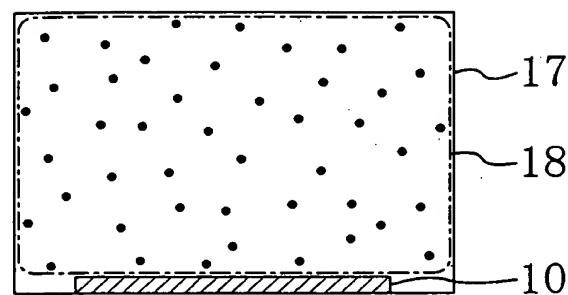


FIG. 3B

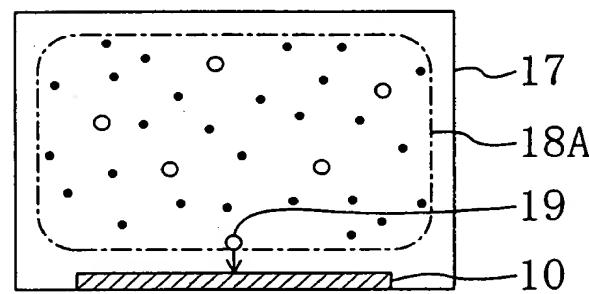


FIG. 3C

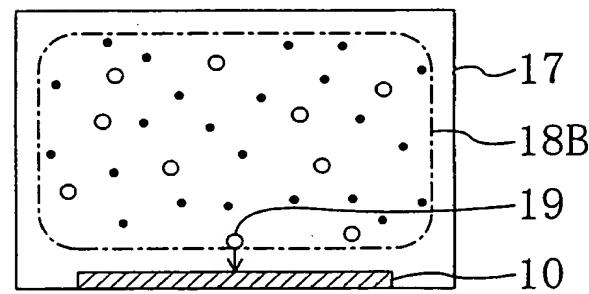




FIG. 4

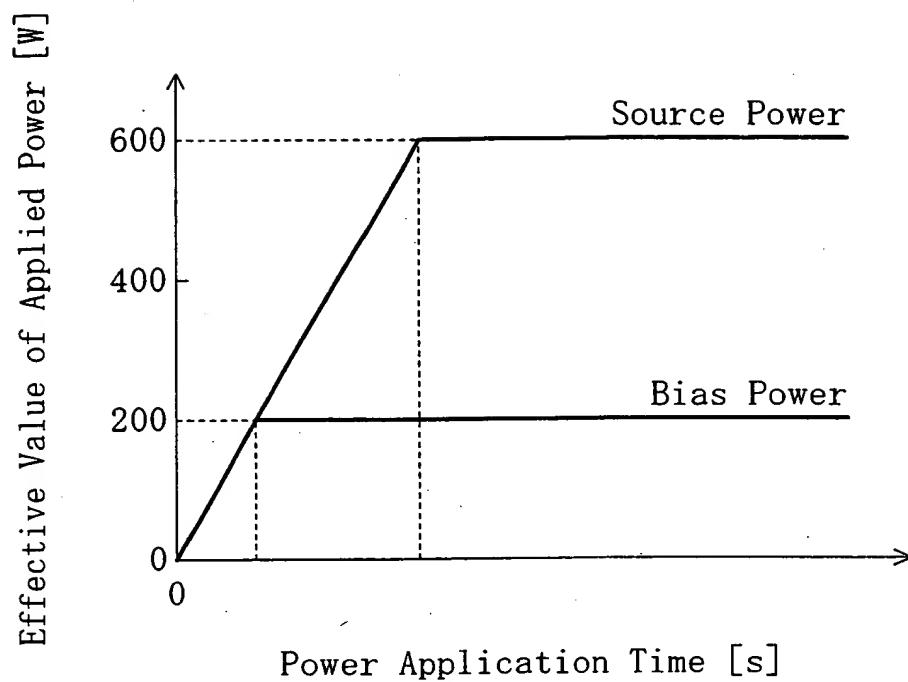




FIG. 5A

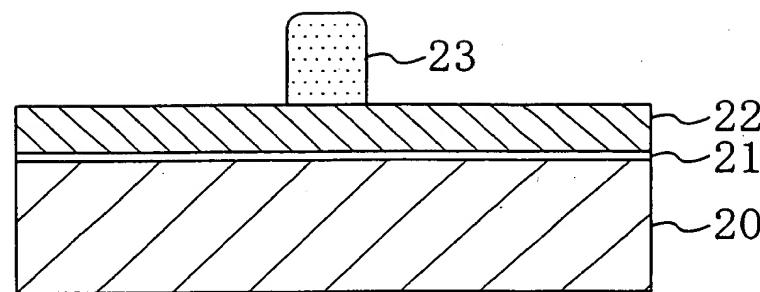


FIG. 5B

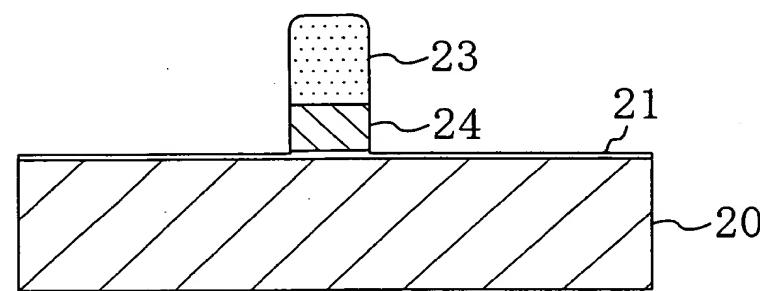


FIG. 5C

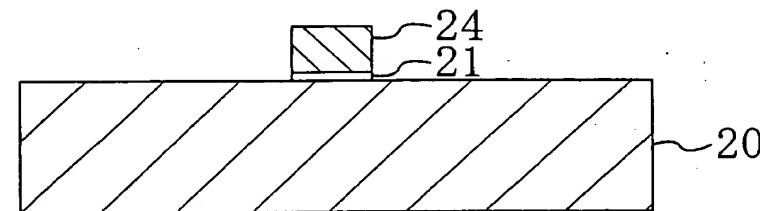




FIG. 6A

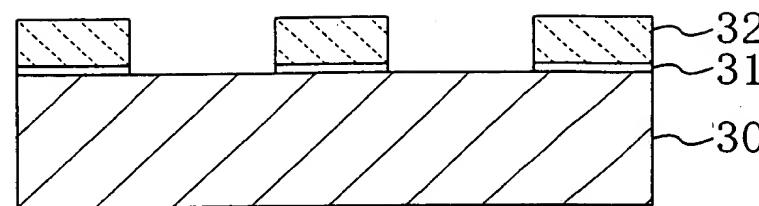


FIG. 6B

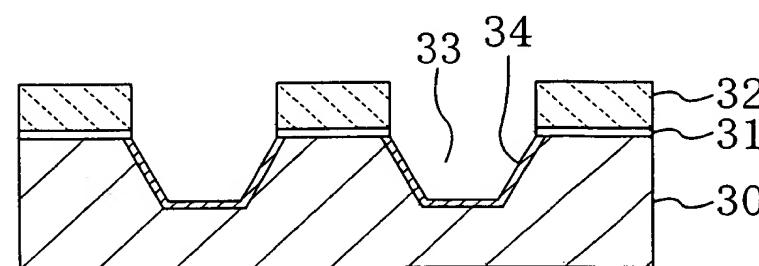


FIG. 6C

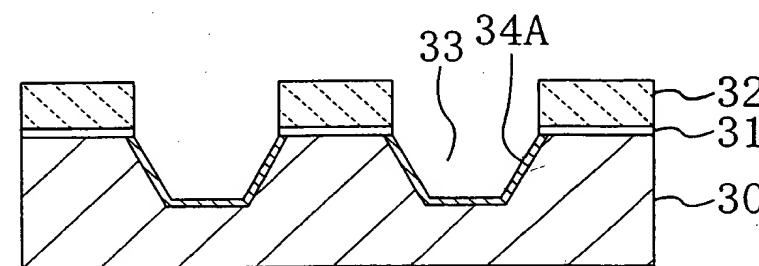


FIG. 6D

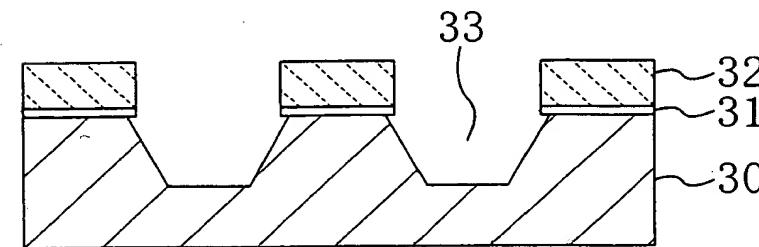


FIG. 6E

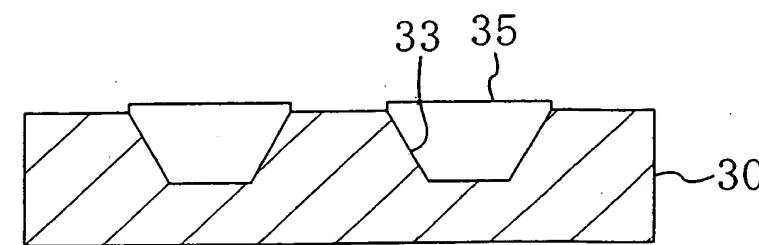




FIG. 7A

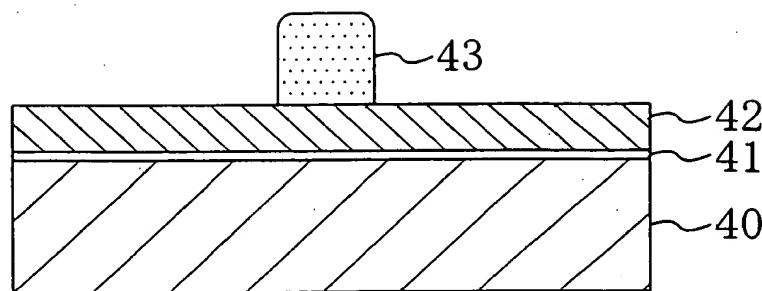


FIG. 7B

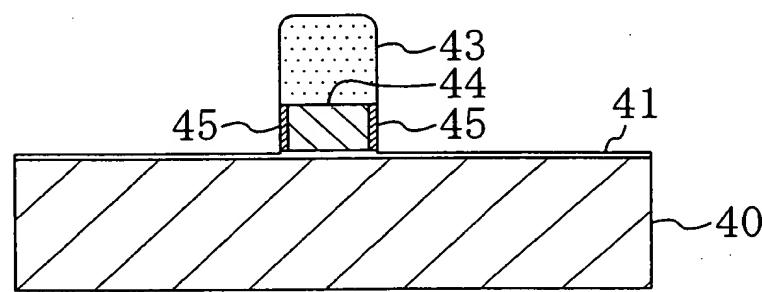


FIG. 7C

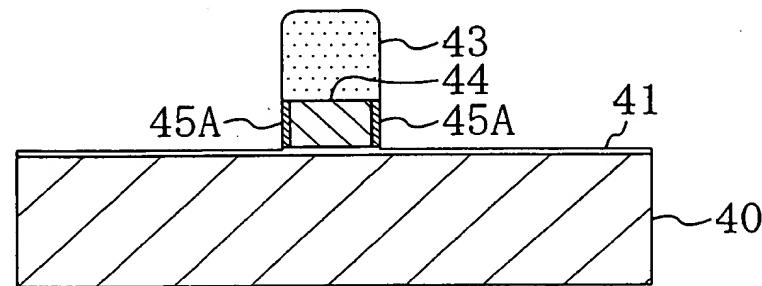


FIG. 7D

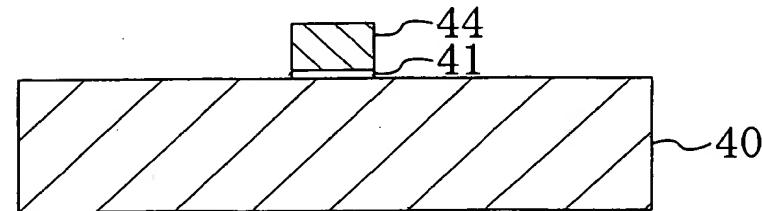




FIG. 8A

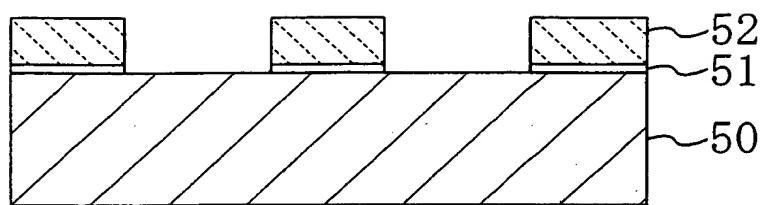


FIG. 8B

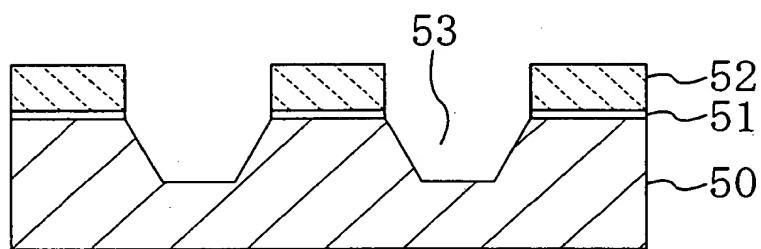


FIG. 8C

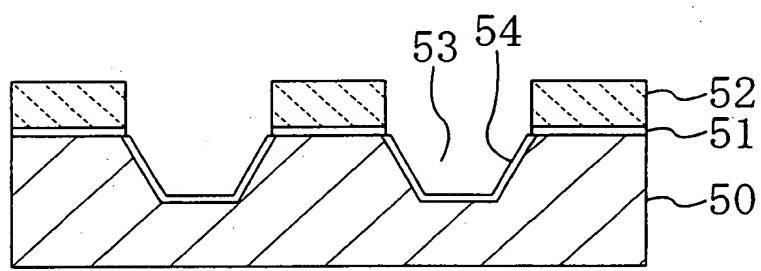


FIG. 8D

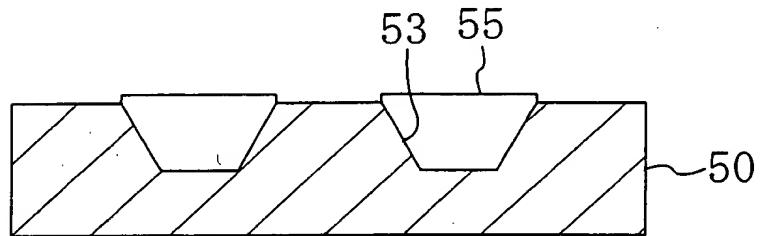




FIG. 9

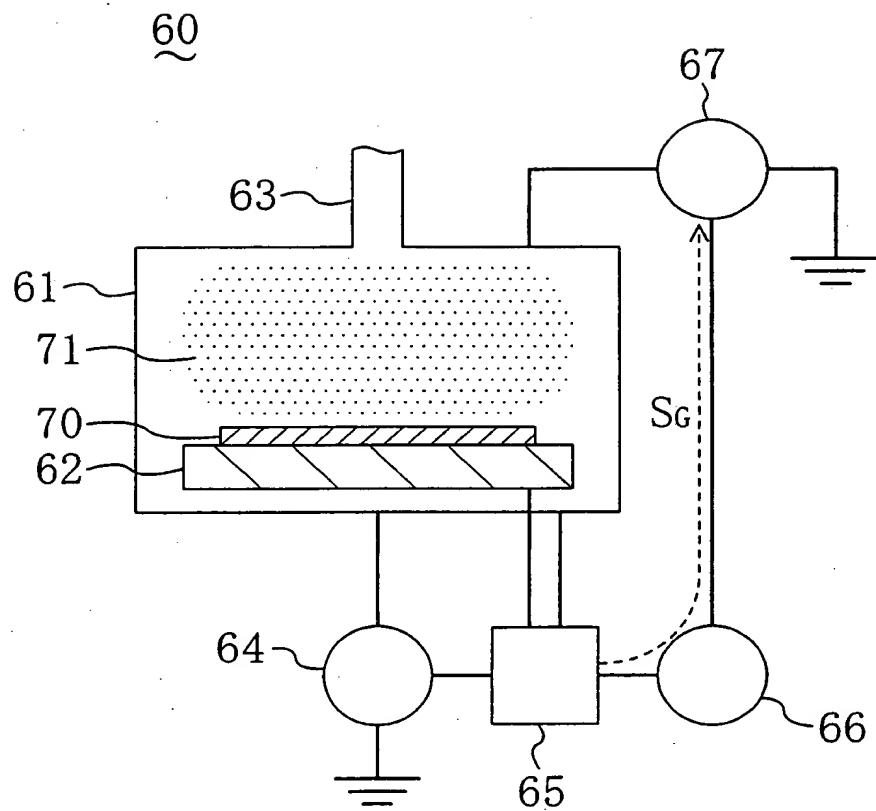




FIG. 10

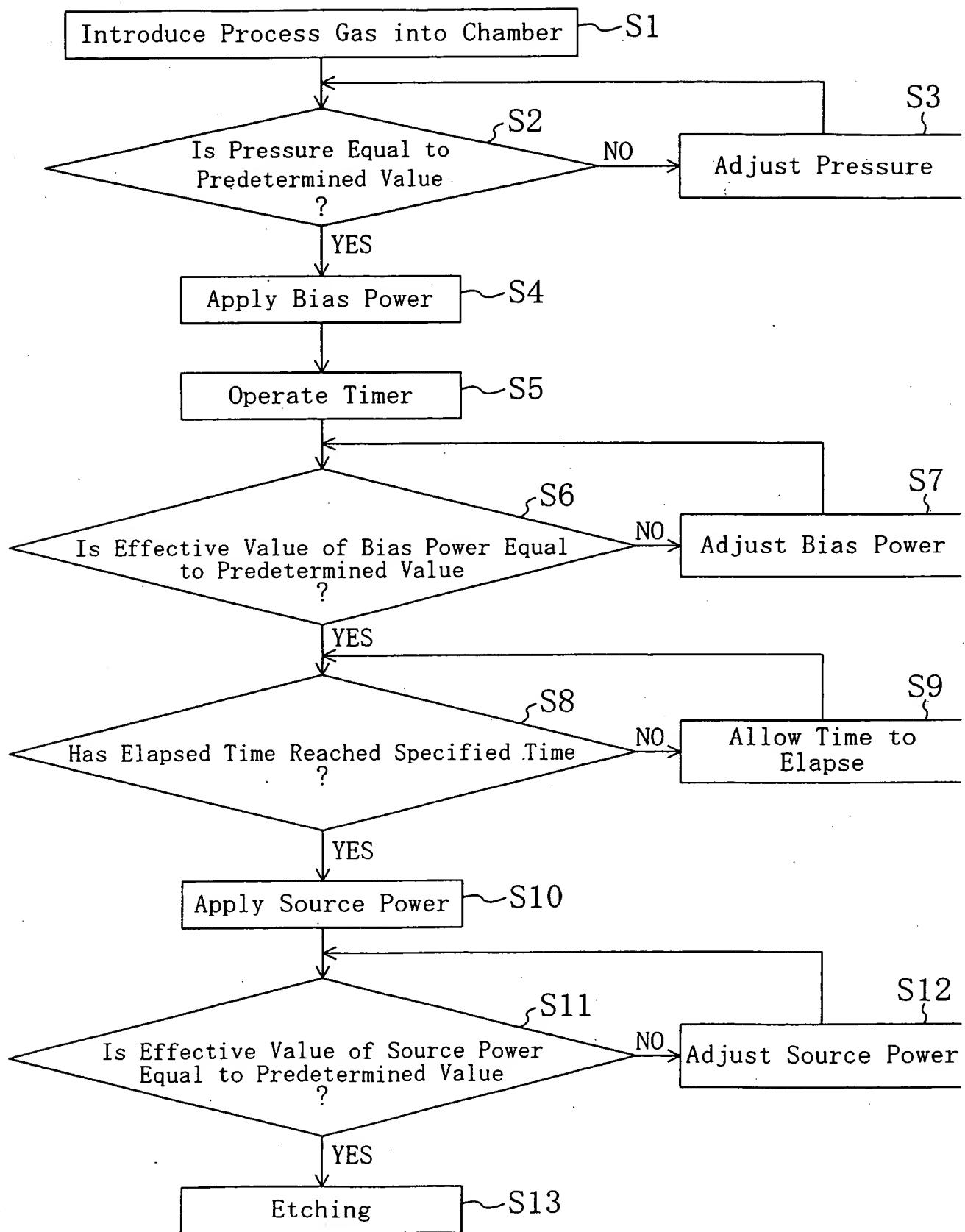




FIG. 11A
Prior Art

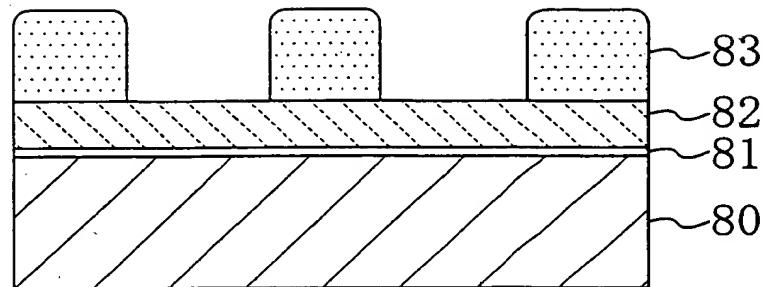


FIG. 11B
Prior Art

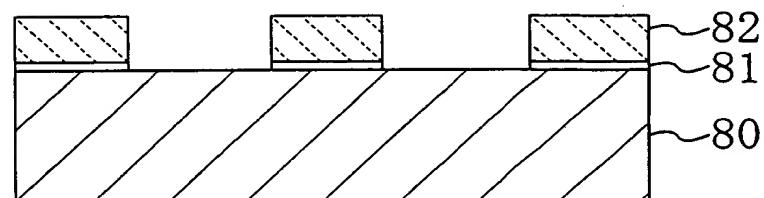


FIG. 11C
Prior Art

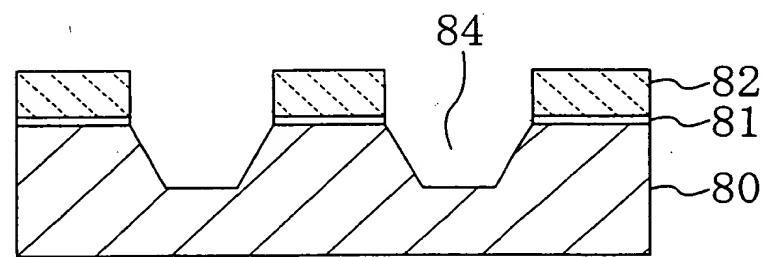


FIG. 11D
Prior Art

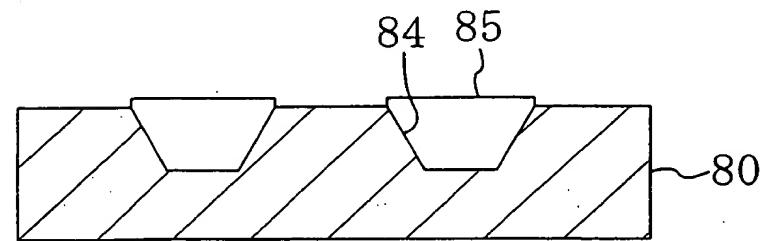




FIG. 12
Prior Art

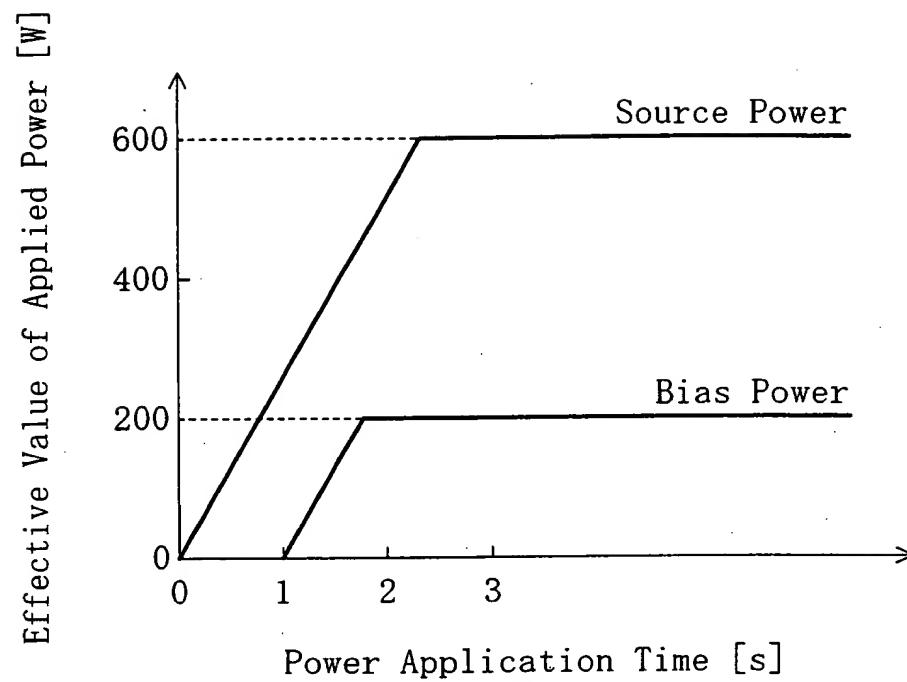




FIG. 13A
Prior Art

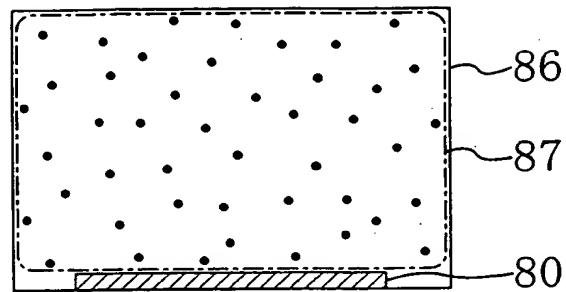


FIG. 13B
Prior Art

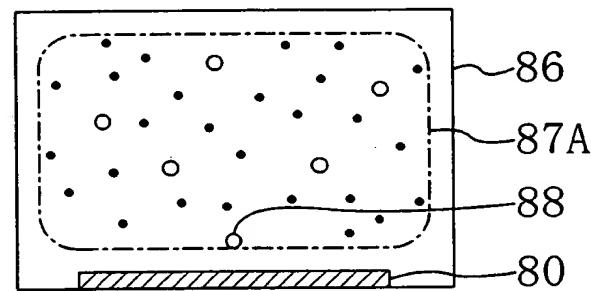


FIG. 13C
Prior Art

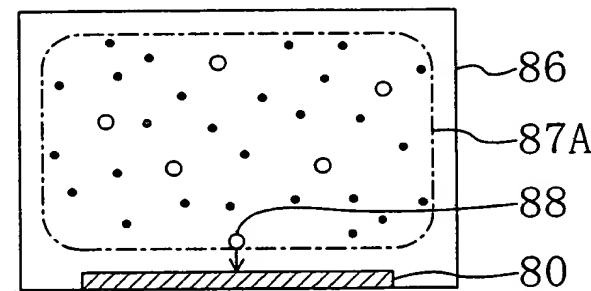




FIG. 14
Prior Art

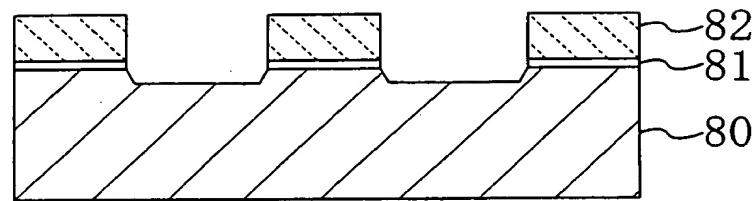




FIG. 15A

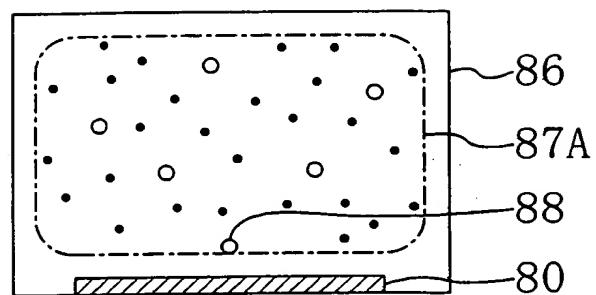


FIG. 15B

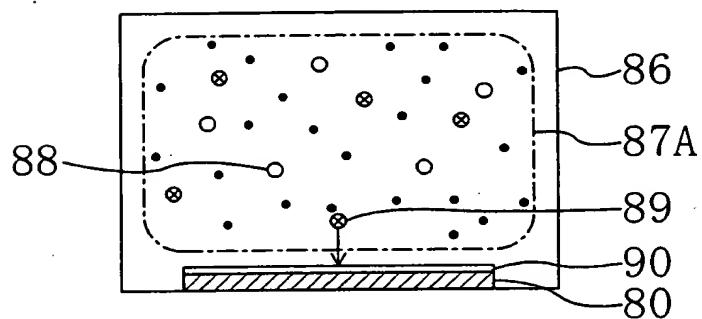


FIG. 15C

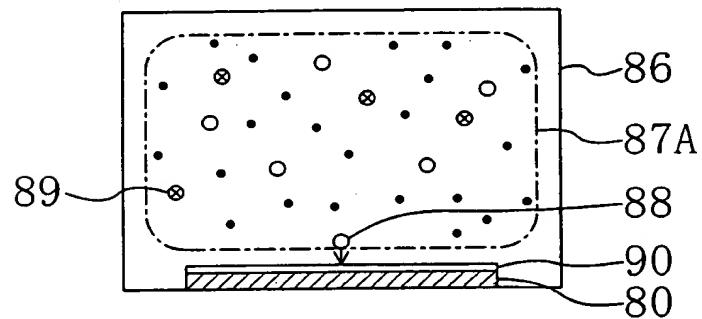
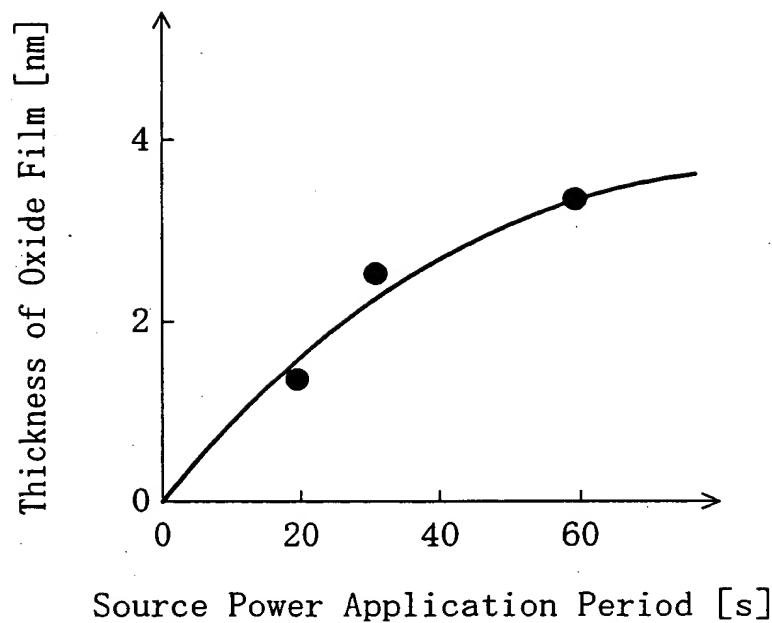


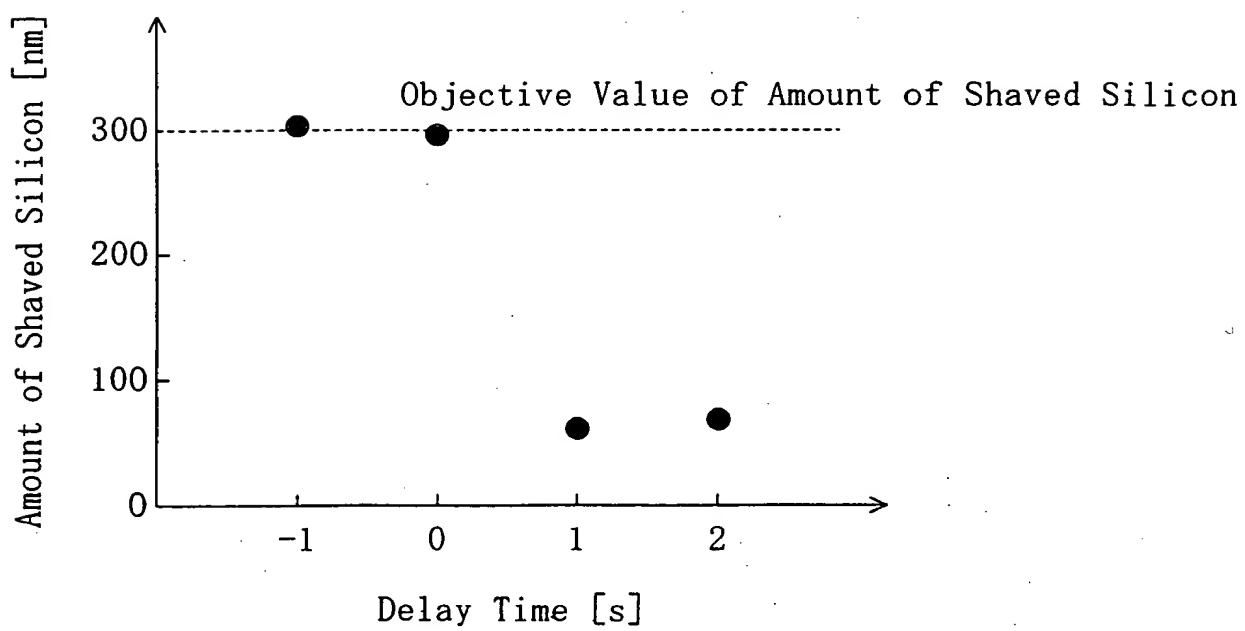


FIG. 16



Source Power Application Period [s]

FIG. 17



Delay Time [s]